



## Asia Symposium on Quality Electronic Design Announces Final Program

Aug. 3<sup>rd</sup> and 4<sup>th</sup>, Penang, Malaysia

**SAN JOSE**, CA and **PENANG** Malaysia, – July 12, 2010 – The [Asia Symposium on Quality Electronic Design](#) (ASQED 2010) today announced its [final program](#) consisting of several keynote speeches by industry leaders and experts from Synopsys, Texas Instruments, University of Tokyo, Elabs and Silicon Hive, tutorials, and over 70 technical presentations.

This is the 2nd annual ASQED event in Asia. The Conference is being held on August 3-4, 2010 in Penang, Malaysia. ASQED plays a critical role in promoting quality-based electronic design and manufacturing in Asia and is an integral part of establishing a communication link between semiconductor, Nano-Electronic, Bio-Electronic, MEMS/NEMS, PV technologies, and disciplines such as design, manufacturing, test, and packaging.

### Powerful Keynotes Headline Conference

The list of plenary keynoters offers a comprehensive perspective from design, process, and tool methodologies. Keynoters and their topics include:

Creating a More Sustainable World through Smarter Design

*John Chilton, Senior Vice President, Marketing & Strategic Development, Synopsys*

Neuromorphics: The reality of nanoscale devices as part of future System on System (SoS) integration

*Prof. Kamran Eshraghian, founder and President, ELabs*

Bio-inspired Electronics based on Spin and Dipole Fluctuated System

*Dr. Hitoshi Tabata, Professor, Head of Department of Bioengineering, The University of Tokyo*

Analog IC Market Trends: The OLD becomes NEW again

*Paul Emerson, General Manager, [Texas Instruments](#)*

Leveraging parallel processing in SoCs

*Dr. Jeroen Leijten, Co-Founder and Chief Technology Officer, Silicon Hive*

### Tutorials

ASQED 2010 offers the following tutorials by industry experts:

Terascale Computing and Interconnect Challenges: 3D Stacking Considerations

*Dr. Tanay Karnik, [Intel](#)*

Planar and Nonplanar Structures Thermal Modeling Studies

*Dr. Rajiv V. Joshi, Research Scientist, IBM T.J. Watson Research Center*

ESD + RFIC Co-Design for Whole-Chip Design Optimization

*Prof. Albert Wang, Dept of Electrical Engineering, University of California*

IPC Standards and Electronics Manufacturing Environment

*David W. Bergman, Vice President, IPC*

## Technical Sessions

ASQED 2010 technical session consists of over 70 presentations by engineers and researchers worldwide, covering the following topics:

- Circuit & System Design
- Test & Verification
- IC Packaging Technology
- PCB and PWB Technology & Manufacturing
- Semiconductor & Nano Technology
- Bio Electronics Innovations
- Photovoltaic Technology & Manufacturing
- Electronic Design Automation Methodologies
- Micro-Electro-Mechanical System (MEMS)

Papers will be presented in three parallel tracks on Tuesday, Aug. 3<sup>rd</sup> and Wednesday, Aug. 4<sup>th</sup>.

## Sponsors, Supporters and Exhibits

ASQED 2010 corporate sponsors and supporters include [Synopsys](#) (Titanium Sponsor), [Cadence Design Systems](#), [SiTerra](#) and [MIMOS](#). ASQED also includes company exhibits.

## About ISQED

ASQED2010 is the second event in Asia organized by the [International Society for Quality Electronic Design](#) (ISQED). ASQED 2010 is co-organized with the Malaysian Industry Government Group for High Technology (MIGHT) and the Malaysian Institute of Microsystems (MIMs) with support from the Malaysian Industrial Development Authority (MIDA). Conference proceedings will be published by IEEE and posted in the IEEE digital library.

Please refer to the conference website at <http://www.asqed.com> for further information about the conference.

## Editorial Contact

Georgia Marszalek  
ValleyPR for ASQED  
Georgia@ValleyPR.com  
+1.650.345.7477